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PATENT

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

|                                                        |                                     |
|--------------------------------------------------------|-------------------------------------|
| In re application of: Hung Pin Kao <i>et al.</i>       | ) Examiner: Not Yet Assigned        |
|                                                        | )                                   |
| Serial No.: Not Yet Assigned                           | ) Art Unit: Not Yet Assigned        |
|                                                        | )                                   |
| Filed: September 13, 2000                              | ) <u>UTILITY PATENT APPLICATION</u> |
|                                                        | ) <u>TRANSMITTAL</u>                |
| For: <b>Side light activated microfluidic channels</b> | )                                   |
| _____                                                  | )                                   |



BOX PATENT APPLICATION

Director of Patents and Trademarks  
Washington, D.C. 20231

Sir:

This is a request for filing a patent application under 37 C.F.R. § 1.53(b) in the name of inventors: Hung Pin Kao and Xing Yang.

For: **Side light activated microfluidic channels**

This application claims priority of U.S. Patent Provisional Application Serial No. 60/153,644 filed on September 13, 1999 under 35 U.S.C. § 119(e).

Application Elements:

19 Pages of Specification, Claims and Abstract

6 Sheet of formal Drawings

CERTIFICATE OF EXPRESS MAILING

"Express Mail" Label No EL637193773US

Date of Deposit September 13, 2000

I hereby certify under 37 C.F.R. 1.10 that this correspondence is being deposited with the United States Postal Service as "Express Mail Post Office to Addressee" with sufficient postage on the date indicated above and is addressed to BOX PATENT APPLICATION, Commissioner of Patents and Trademarks, Washington, D.C., 20231

\_\_\_\_\_  
 (Signature) *Jessica Alden*  
 \_\_\_\_\_  
 (Printed Name)

09/13/00 09/660992

☒ Declaration

☒ Unexecuted Combined Inventor Declaration and Power of Attorney

☐ Copy from prior application (37 CFR 1.63(d) for a continuation or divisional).

The entire disclosure of the prior application from which a copy of the declaration is herein supplied is considered as being part of the disclosure of the accompanying application and is hereby incorporated by reference therein.

☐ Deletion of inventors Signed statement attached deleting inventor(s) named in the prior application, *see* CFR 1.63(d)(2) and 1.33(b).

Accompanying Application Parts:

☐ Assignment and Assignment Recordation Cover Sheet (recording fee of \$40.00 enclosed)

☐ Power of Attorney

☐ 37 CFR 3.73(b) Statement by Assignee

☐ Information Disclosure Statement with Form 1449

☐ Copies of IDS Citations

☐ Preliminary Amendment

☒ Return Receipt Postcard

☒ Small Entity Statement(s) ☒ Statement filed in prior application.

Status still proper and desired.

☐ Other:

☐ A sequence listing.

☐ paper copy.

☐ computer readable copy.

☐ Statement in Compliance with Requirements for Patent Applications Containing Nucleotide and/or Amino Acid Sequence.

Claim For Foreign Priority

☐ Priority of \_\_\_\_\_ Application No. \_\_\_\_\_ filed on \_\_\_\_\_

is claimed under 35 U.S.C. § 119

☐ The certified copy has been filed in prior application U.S. Application No. \_\_\_\_\_

☐ the certified copy will follow.

Extension of Time for Prior Pending Application

☐ A Petition for Extension of Time is being concurrently filed in the prior pending application. A copy of the Petition for Extension of Time is attached.

Amendments

- ☐ Amend the specification by inserting before the first line the sentence: "This is a  
☐ Continuation ☐ Continuation-in-part ☐ Nonprovisional application of  
copending prior  
☐ Application Serial No. 60/153,644 filed on September 13, 1999.  
☐ International Application \_\_\_\_\_ filed on \_\_\_\_\_, which  
designated the United States, disclosure of which is incorporated herein by reference."  
☐ Cancel in this application original claims \_\_\_\_\_ of the prior application before calculating  
the filing fee

Fee Calculation (37 CFR § 1.16):

|                                                  | (Col. 1)         | (Col. 2)     | OTHER THAN A        |              |                     |            |
|--------------------------------------------------|------------------|--------------|---------------------|--------------|---------------------|------------|
|                                                  |                  |              | <u>SMALL ENTITY</u> |              | <u>SMALL ENTITY</u> |            |
| <u>FOR:</u>                                      | <u>NO. FILED</u> | <u>NO.</u>   | <u>RATE</u>         | <u>FEE</u>   | <u>RATE</u>         | <u>FEE</u> |
|                                                  |                  | <u>EXTRA</u> |                     |              |                     |            |
| Basic Fee                                        |                  |              | \$345               | \$345        | \$690               | \$         |
| Total Claims                                     | 24               | 4            | \$ 9                | \$36         | \$ 18               | \$         |
| Indep. Claims                                    | 5                | 2            | \$ 39               | \$78         | \$ 78               | \$         |
| <input type="checkbox"/> Multiple<br>Dep. Claims |                  |              | \$130               | \$0          | \$260               | \$         |
| Total Filing Fee:                                |                  |              |                     | <b>\$459</b> |                     |            |

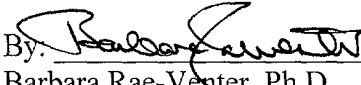
TOTAL FEES: \$459.00

- ☒ A check including the amount of **\$384.00** is attached.  
☒ Please charge Deposit Account No.18-0020 in the amount of **\$65.00**.  
☐ No fee is required.

- [X] Conditional Petition for Extension of Time: An extension of time is requested in the present and/or the above-referenced parent application to provide for timely filing if an extension of time is still required after all papers filed with this transmittal have been considered.
- [X] The Commissioner is hereby authorized to charge any underpayment of the following fees associated with this communication, including any necessary fees for extension of time, or credit any overpayment to Deposit Account No. 18-0020.
- [X] Any filing fees under 37 CFR 1.16 including fees for the presentation of extra claims.
- [X] Any parent application processing fees under 37 CFR 1.17.
- [X] A **duplicate** copy of this sheet is attached for accounting purposes.

Respectfully submitted,

Dated: September 13, 2000

By:   
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Enclosures

**APPLICATION**  
**FOR**  
**UNITED STATES LETTERS PATENT**

**TITLE: SIDE LIGHT ACTIVATED MICROFLUID  
CHANNELS**

**INVENTORS: HUNG PIN KAO, XING YANG**

Certificate of Express Mailing

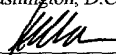
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(Signature)

(Printed Name)

  
Jessica Alden

**SIDE LIGHT ACTIVATED MICROFLUID CHANNELS****CROSS-REFERENCE TO RELATED APPLICATIONS**

5           This application claims the benefit of the filing date of U.S. provisional application 60/153,644 filed September 13, 1999, which disclosure is hereby incorporated by reference.

**INTRODUCTION****10   TECHNICAL FIELD**

          The field of this invention is irradiation and detection of light emitting entities in microchannels and the fabrication of devices for this purpose.

**15   BACKGROUND**

          Microfluidics employs small capillary channels or microchannels in a solid substrate to perform a wide variety of operations. By employing electrical fields with conductive fluids in the microchannels, very small volumes may be accurately moved, reagents mixed, and the presence of an entity of interest determined. In many applications, the determination is fluorescence. Fluorescence can provide for high sensitivity, allows for multiplexing, by detecting photons at different wavelengths; by appropriate use of combinations of fluorophores one can employ a single light source for excitation. In addition, there are a large number of different commercially available detection devices. In addition, many assays have been developed which depend upon the use of fluorescence, such as DNA sequencing, receptor assays dependent upon expression of green fluorescent protein, immunoassays employing a fluorescent label, etc.

          In the case of microfluidics, the use of plastics as the substrate has found application. While plastics have the advantage of ease of fabrication, cost and ready availability, they tend to be fluorescent. In addition, when irradiating a sample with excitation light, light scatter results in a significant background signal, particularly when the excitation pathway and emission pathway are the same. While some systems provide for an obtuse angle between the excitation and emission optical pathways, in

the case of microfluidics there are the problems of directing the excitation light to the center of the microchannel, the small amount of sample that will be exposed to the irradiation and the substantially diminished fluorescent signal that one observes at an angle from the microchannel. Also, since the excitation light will encounter the device substrate, there is the further fluorescence of the substrate added to the fluorescent signal.

There is, therefore, an interest in designing new approaches to excitation of fluorophores in microchannels and detecting the fluorescent emission efficiently.

#### BRIEF DESCRIPTION OF RELEVANT ART

U.S. Patents of interest include 4,863,560, "Fabrication of Silicon Structures by Single Side, Multiple Step Etching Process"; 5,006,202, "Fabrication Method for Silicon Devices Using a Two Step Silicon Etching Process"; and 5,738,757, "Planar Masking for Multi-Depth Silicon Etching." Publications of interest include Backlund and Rosengren, "New shapes in (100) Si using KOH and EDP etches," J. Micromach. Microeng. 1992, 2:75-79; Sekimura and Naruse, Fabrication of 45° optical mirrors on (100) silicon using surfactant-added TMAH solution," International Conference on Solid State Sensors and Actuators, pp. 550-551, Sendai, Japan, June 7 - 10, 1999; Strandman, et al., "Fabrication of 45° Mirrors Together with Well-Defined V-grooves Using Wet Anisotropic Etching of Silicon, J. of Microelectromechanical Systems (MEMS) and Chang and Hicks, "Mesa structure formation using potassium hydroxide and ethylene diamine based etchants." IEEE Workshop on Solid State Sensors and Actuators, pp. 102-103, Hilton Head, SC, June 1988.

#### SUMMARY OF THE INVENTION

Methods and devices are provided for an optical system for emission detection from microchannels in plastic substrates. A master is formed for plastic molding of a microfluidic device, where the device has parallel fluid streams, optionally separated

with at least substantially perpendicular side walls, and on each side of said streams is a microfabricated optic, having reflecting walls for directing a light beam through the streams and then into a waste light dump. The master is formed by separately etching different microstructures, with appropriate masking and different protective coatings, which are individually removed prior to final etching to provide deep microstructures. Microfabrication techniques are provided for molding microfluidic devices employing the optical system for use in fluorescent based operations.

#### BRIEF DESCRIPTION OF THE FIGURES

Fig. 1 is a diagrammatic side view of an etched silicon wafer;

Fig. 2a – p are diagrammatic views of a silicon wafer during the stages of the processing of the wafer;

Fig. 3 is a diagrammatic view of a group of electrokinetic capillary channels with two optical elements;

Fig. 4 is a diagrammatic view of a device with a plurality of sample channels interspersed with sheath flow channels; and

Fig. 5 is a diagrammatic plan view of an exemplary capillary channel and optical channel layout.

#### DESCRIPTION OF THE SPECIFIC EMBODIMENTS

The subject invention provides microfluidic devices comprising optical systems, methods of making the microfluidic devices and methods of using the microfluidic devices. The microfluidic devices comprise a fluid streambed with at least one stream, usually at least two streams. The individual streams may be divided by at least substantially 90° walls parallel to each other and desirably perpendicular to the base or floor of the microchannel, or individual streams separated by liquid separators, creating a plurality of microchannels having parallel regions. Disposed on each side of the fluid stream bed are optical elements comprising a first reflective wall for receiving a light beam and directing the light beam through the fluid stream bed and a second reflective



wall for receiving the light beam from the fluid stream bed and directing the light beam to a light waste dump.

To produce the microfluidic devices, a positive form (same structure as the final product) is fabricated, which form is used to make a mold for molding microfluidic devices from plastics. The positive form is fabricated by initially providing differential protective coatings over sites of different microstructures using appropriate masking and protective coatings, followed by deep etching the different microstructures independently after differential removal of the protective coatings. The resulting devices may be used in microfluidic operations, where the fluid streambed of parallel streams is used to detect fluorescence from a fluorophore present in the streambed region. A light source directs light through the fluid streambed by means of the optical elements and the fluorescent light is detected normal to the direction of the light beam. In this way, the signal-to-noise ratio is greatly enhanced, interference from fluorescence of the substrate used to produce the device is minimized and simple optical excitation and emission detection systems may be employed.

In producing the microfluidic devices, a form is fabricated from which a mold can be produced. The form is conveniently made from a silicon wafer using microfabrication techniques. The subject method for preparing the form employs a planar masking process for etching deep silicon structures with different cross-sectional profiles that allows patterning of multiple structures before etching while the wafer surface is still planar. The process also provides protection for the previously etched structures during the later etching steps using selective thin-film deposition/growth and, therefore eliminates photoresist spin coating on deep etched structures. In carrying out the process, the channels are produced at one stage and the optical elements at another stage, by employing appropriate masking, protective coatings and etching.

The methodology uses semiconductor lithographic technology. The process involves forming and partially removing layers of silicon dioxide and silicon nitride, enlisting the use of photoresist layers as masks and etching portions of the underlying substrate and/or protective silicon oxide/nitride coatings unprotected by the photoresist. Chemical vapor deposition is employed for providing protective layers of silicon

nitride, where layers of silicon oxide and silicon nitride may be differentially removed. The method is described in greater detail in reference to Fig. 2, where the device has walls for separating the different streams in the fluid streambed.

5           The process is used to produce silicon chips having microstructures in said silicon chip of different cross-sections and depths of greater than about  $5\mu\text{m}$ , usually greater than about  $10\mu\text{m}$  and may be  $100\mu\text{m}$  or more, where depths of  $5\mu\text{m}$  create problems of having a uniform photoresist coated using spin coating. The method comprises the steps of: growing silicon dioxide on both sides of a silicon wafer; spin-  
10   coating both sides of said wafer with photoresist; photolithographically patterning a first side of said wafer to define sites for at least one of a first set of microstructures;

          etching said silicon dioxide layer at sites above areas to be etched to form at least one of a first set of microstructures; stripping said photoresist from said wafer; depositing a silicon nitride layer on both sides of said silicon wafer; spin-coating on  
15   both sides of said wafer with photoresist; photolithographically patterning said first side of said wafer to define sites for at least one of a second set of microstructures having a different cross-section from said first set of microstructures; etching said silicon nitride layer at sites above areas to be etched to form said second set of microstructures and silicon dioxide underneath said silicon nitride layer to expose said silicon wafer;

20           etching said silicon wafer to form at least one of said second microstructures; coating exposed portions of said wafer with silicon dioxide; removing remaining portions of said silicon nitride layer and exposing said wafer at said sites of said at least one of a first set of microstructures; and etching said silicon wafer to form said first microstructures.

25           By using the process described above, one may prepare devices, where a plurality of liquid streams are irradiated orthogonally to their direction of flow. The device substrate will usually be of a thickness in the range of about 0.2mm to 1cm, more usually 1mm to 5mm. The substrate will usually be enclosed with a transparent  
30   plastic film of from about 5 to  $100\mu\text{m}$  thickness. The plurality of liquid streams may

be separated by walls constructed in the device or by sheath flow, where the device provides a liquid stream to separate the liquid streams comprising the fluorophore to be measured. Each of the devices will have two optical elements on opposite sides of the liquid streams to be irradiated. Generally, the optical elements will be separated by at least about 0.1 mm, more usually at least about 1 mm, and not more than about 25 mm, usually not more than about 10 mm. The spacing between the optical elements will depend upon the number of liquid streams to be irradiated, whether the streams are separated by liquid or solid spacers, the width of the streams, the nature of the light source, the diffusion of the light beam, and the like. There will generally be at least 2 streams between optical elements, more usually at least about 3 streams, and not more than about 12 streams, usually not more than about 10 streams. By using a multiplicity of optical elements on each side of a group of streams, one can provide for as many streams as one wishes to have irradiated. Since each optical element may have two reflective sides, one side may be used for irradiation of the streams and the other side may be used to direct the light from a different optical element, which light has passed through the streams, to a waste light dump. In an alternative embodiment, the optical element for directing light to a waste light dump is not required where the light will be dispersed within the substrate of the device.

The optical element is a channel, which may or may not be the length of the liquid stream. The optical element will have a wall, which reflects the light beam. The depth and length of the optical element need only be sufficient to fulfill its function of directing a light beam. The length of the optical element will usually be at least about 1mm and not more than the length of the stream. The depth will usually be at least about 10% of the depth of the streambed and up to and including the depth of the streambed, although it may be extended further, if desired. Reflection may be as a result of a difference in refractive index between the substrate and the contents of the optical element channel, a reflecting surface inside the channel, e.g. aluminum, silver, etc., or other means for light transfer. Where the optical element channel is between two groups of streams, it will have two reflecting walls, one normally for directing the light beam into the streams and the other for reflecting the light received from the

streams into a light dump. While it is not necessary to have the walls at a  $45^\circ$  angle to the planar surface of the device, this angle is the most convenient, since the light source can be directed normal to the surface of the device and will be reflected at a  $90^\circ$  angle to the light beam from the light source. Otherwise, the light source will be placed at an angle to the optical element channel wall, which provides that the light be reflected in a line or plane parallel to the surface of the device. The angle of the wall will usually be in the range of about  $45 \pm 15^\circ$ .

When solid walls are present in the streambed, they will have a width of about 0.1 to  $100\mu\text{m}$ , more usually about 1 to  $50\mu\text{m}$ . The height of the wall will generally be about 2 to  $500\mu\text{m}$ , more usually about 5 to  $100\mu\text{m}$ , while the spacing between the walls forming the channels will usually be in the range of about 5 to  $500\mu\text{m}$ , more usually about 10 to  $200\mu\text{m}$ . When solid walls are not present in the streambed, namely the streams are separated by liquid walls or streams using sheath flow, separation between streams will be at least about  $10\mu\text{m}$ , more usually at least about  $20\mu\text{m}$ , and not more than about  $100\mu\text{m}$ , usually not more than about  $50\mu\text{m}$ .

The subject devices find particular use with multiplexed devices, which produce a plurality of streams having a fluorescent entity for detection. These devices are exemplified by electrokinetic devices, where the electrokinetic devices have a plurality of units, each unit producing an independent stream for analysis. The subject device may be part of an electrokinetic device, so as to be integrated with the substrate used for forming the electrokinetic units. The electrokinetic devices are characterized by having a channel, which will feed the stream into the detection device of the subject invention. In this instance, the walls of the electrokinetic device may have the same orientation as the walls of the electrokinetic device or the electrokinetic wall channels be tapered to join with the detection device walls

In the situation with the liquid walls or streams, the streams from the electrokinetic units will generally be tapered down to reduce the length of the

irradiation beam through the streams. The electrokinetic device capillary channels will generally have a width in the range of about 10 to 500 $\mu$ m, where the walls or spacing between the channels will usually be in the range of about 10 to 1000 $\mu$ m, more usually in the range of about 100 to 500 $\mu$ m. The tapering area will generally reduce the cross-section of the capillary channels and walls by at least about 30%, preferably at least about 50% and may result in a reduction of 75% or more. The tapering area will have angled sides, where the angle may vary in the range of about 5 to 65°, usually about 45° $\pm$ 15°. The distance between the outlets from the capillary channels and the detection region in the streambed will generally be at least about 0.1mm, more usually at least about 1mm and not more than about 10mm, generally being from about 1 to 5mm, being controlled by the angle of narrowing, the width of the detection region, the width of the channel area feeding the detection region, the depth of the stream bed and the volume of liquid exiting from the capillary channels into the streambed. The width of the detection region will be in the range of about 0.2 to 50mm, more usually about 0.5 to 20mm

The irradiation region will normally be only a small portion of the stream bed channel, which stream bed channel may be straight or tortuous and of the same or different dimensions from the region of the channels in the irradiation region. Where the stream bed does not have dividing walls, the streams emanating from the capillary channels will generally have a stream width in the range of about 0.1 to 50 $\mu$ m, more usually about 1 to 20 $\mu$ m. There will be at least one stream between optical elements and not more than about 100, usually not more than about 50, generally in the range of about 5 to 50. As discussed previously, the number of streams will be controlled by the effectiveness of the radiation beam and its ability to remain sharp while passing through the streams to ensure a predetermined area of exposure of a desired level of intensity.

There may be one or a plurality of groups of streams bordered by optical elements. For a device, there will be at least one group and there may be two or more,

where the groups may be in a single line or be distributed throughout a wafer having a plurality of capillary electrokinetic units. Each of the capillary electrokinetic units will usually have at least three reservoirs and two intersecting channels, where the intersections may be a cross-intersection or a T-intersection. Of course, the units may be much more sophisticated, where a greater number of chambers and channels may be involved, including mixing chambers, additional reservoirs, separation channels, injection channels, etc. Also, there will usually be at least three, usually at least four electrodes, where the electrodes are used to create electrical fields in the channels to move particles by electrokinesis, which includes electrophoresis and electroosmotic force or flow ("EOF").

The subject devices may be prepared as follows. A silicon wafer cut about 9.74° off the <100> orientation is employed to form the (opposite configuration from the final product), which may then be used to produce a negative mold for forming the devices in plastic. The first step is growing a silicon dioxide layer on at least one surface of said silicon wafer. This is followed by photolithographically etching one side of said wafer to remove said silicon dioxide at predetermined sites for channel formation. The silicon wafer is then coated on at least the side in which the channels are to be formed with silicon nitride. To form an optical channel, the channel side is photolithographically etched to expose the silicon at an optical channel site, followed by etching the optical channel site with anisotropic silicon etchant, e.g. hydroxide (5 to 40%) at an elevated temperature (40 to 100°C) to produce a V-shaped groove at a 45° angle. The exposed silicon in the optical channel is oxidized to silicon oxide, followed by removing remaining silicon nitride and etching the silicon wafer through said silicon dioxide layer to form channels with 90° separating walls. The resulting positive mold may then be used to produce a negative mold to be used for fabricating the subject devices. In an alternative approach, the wafer substrate may be aluminum or some other appropriate composition where microfeatures are cut into the substrate through diamond ruling or turning.

To demonstrate the subject invention a chip was prepared as follows:

In Figs. 1a and 1b are side diagrammatic views of a wafer indicating the angles resulting from etching, where the wall angle varies with the manner in which the wafer is etched. In the common wet etching of  $\langle 100 \rangle$  silicon wafers, the angle formed between  $\langle 100 \rangle$  and  $\langle 111 \rangle$  is  $54.74^\circ$  as shown in Fig. 1(a). To make a  $45^\circ$  angle, the approach of using wafers that were cut  $9.74^\circ$  off the  $\langle 100 \rangle$  orientation was used. Such wafers were purchased from Virginia Semiconductors.

Fig. 2 shows the fabrication process. First, a  $0.2\text{-}3\mu\text{m}$  ( $2\mu\text{m}$ ; the actual exemplification will be indicated in parentheses) thick silicon dioxide layer 202 (Fig. 2(a)) is thermally grown on both sides of a silicon wafer 201. Then,  $0.1\text{-}4\mu\text{m}$  ( $2\mu\text{m}$ ) thick Shipley S1818 photoresist 203 is spin-coated on both sides of the wafer, as shown in Fig. 2(b). The photoresist layer 203 on the wafer front side is photolithographically patterned (Fig. 2(c)). The silicon dioxide layer 202 then is etched (Fig. 2(d)) in a hydrofluoric acid solution buffered with ammonium fluoride, using photoresist layer 203 as the mask. The photoresist layer 203 at the back side of the wafer protects the silicon dioxide layer 202 from being etched. After the photoresist layer 203 is stripped, the wafer 201 is left with the silicon dioxide layer 202 shown in Fig. 2(e). In Fig. 2(f),  $0.2\text{-}2\mu\text{m}$  ( $0.5\mu\text{m}$ ) thick silicon nitride layer 204 is deposited on both sides of the silicon wafer 201 using low pressure chemical vapor deposition (LPCVD). Another layer of photoresist 205 is spun on both sides of the wafer (Fig. 2 (g)) and photolithographically patterned (Fig. 2(h)). The silicon nitride layer 204 is etched using  $\text{SF}_6/\text{O}_2$  plasma etching process with photoresist layer 205 as the mask shown in Fig. 2(i). In Fig. 2(j), the underlying silicon dioxide layer 202 is etched in hydrofluoric acid solution buffered with ammonium fluoride. The photoresist layer 205 is stripped (Fig. 2(k)). The wafer 201 is then etched in 5 – 40% (20%) potassium hydroxide solution at  $40\text{--}100^\circ\text{C}$  ( $60^\circ\text{C}$ ) (Fig. 2(l)) with both silicon dioxide 202 and silicon nitride 204 as the mask layer. A  $50\mu\text{m}$  deep V-shape groove with one angle of  $45^\circ$  is formed. In Fig. 2(m), another  $2\mu\text{m}$  thick silicon dioxide layer 206 is grown on the wafer. Because most of the wafer surface is covered with silicon nitride layer 204, only the exposed V

groove silicon surface will be oxidized due to the extremely low oxidization rate of silicon nitride surface. This layer of silicon dioxide layer 206 protects the structures etched in the previous steps from being etched in the later etching steps. In Fig. 2(n), the silicon nitride layer 204 is etched using  $\text{SF}_6/\text{O}_2$  plasma etching without using a mask layer. The  $\text{SF}_6/\text{O}_2$  plasma etching process has relatively high etching selectivity of silicon nitride 204 over silicon dioxide 202 and 206. Because of this, the silicon nitride layer 204 is completely removed while the silicon dioxide layer 202 and 206 is not etched significantly. The silicon wafer 201 is then etched (Fig. 2 (o)) using a deep reactive ion etching (DRIE) process to form 50  $\mu\text{m}$  deep channels with vertical sidewalls. Finally, the silicon dioxide layer 202 and 206 is stripped (Fig. 2(p)).

In Fig. 3 is a diagrammatic view of a group of channels with two optical elements. The device 300 has several light beams 301 from light sources (not shown). The light beams 301 are conveniently a laser beam. The device 300 has microfabricated capillary channels 302 and microfabricated optical channels 303, having walls 304, which are shown at a  $45^\circ$  angle. The microfabricated optic 303 is a microfluidic channel, where reflection may be accomplished by coating the surface of the channel with a reflective layer, e.g. aluminum, or the channeled may be filled with air or other material of a different refractive index from the wall material, such that total internal reflection occurs when the laser beam strikes the wall 304 surface. The wall 304 should be optically flat. The channel surfaces or walls 305 should also be substantially optically flat and at a  $90^\circ$  angle to the planar surface of device 300 and, therefore, normal to the laser beam propagation 301. With fluorophores in the channels 302, fluorescence emission is detected perpendicular to the device 300 by an optical system 306. Any convenient optical detection system may be employed, e.g. a CCD, photocells, photodiode, photomultiplier tubes etc. The autofluorescence from the walls 307 separating the channels 302 will provide a very small contribution to the fluorescence emitted from the channel 302.

In Fig. 4, the device 400 comprises a plurality of sample channels 401 interspersed with sheath flow channels 402. The sample channels 401 have outlets 403



and the sheath flow channels 402 have outlets 404 for directing the flow of liquid into streambed 405. For the most part, the liquid in sample channels 401 will be an aqueous electrically conductive liquid, having a component to be measured.

Frequently, the liquid carrier will be an aqueous buffer. The sheath flow liquid may be

5 the same liquid as the carrier or a different liquid, having the same or different composition and viscosity. The rate of flow of the streams of sample liquid and sheath flow liquid will be adjusted to provide the desired width of the sample liquid in the detection region 406 and the desired spacing between sample streams. Optical elements 407 and 408 are positioned on opposite sides of the detection region 406, whereby light  
10 reflected from optical element 407 is propagated through the detection region 406 and the sample streams and reflected into a light dump by optical element 408.

Fluorescence emitted from detection region 406 is detected by an appropriate fluorimetric detection system, not shown.

15 The device may be modified by having microlenses integrated into the top plate. As seen in Fig. 6, the microfabricated device may have several integrated microlenses 601 incorporated into the top plate to substantially capture more fluorescence 602 excited within the microfluidic channels 603. The walls 604 of channels 602 may or may not be substantially optically flat and may or may not be precisely at a 90° angle,  
20 usually deviating at most by about 10° from the planar surface of the device 600. The light beam 605 used to excite the fluorescence within the microfluidic channels may propagated through the channels at an angle substantially parallel to the planar surface of the device or deviate from such angle, usually by not more than about 10°. In addition to the microlenses 601, further focussing of the light from the individual  
25 channels onto a detector array or fiber optic array leading to a detector array may be implemented by a focussing lens system 606.

In Fig. 5 is a diagrammatic plan view of the layout of the chip as employed in the subject invention and as depicted in cross-section in Fig. 3. The microfluidic  
30 device 500 has a plurality of capillary channels with differing lengths. Two of the

microfluidic channels 504 and 506 have injection cross-channels 508 and 510, respectively. Each of the channels has reservoirs 512 at opposite ends of the channel, in which electrodes, not shown, are placed. By appropriate activation of the electrodes, either electroosmotic flow or electrophoretic flow occurs in the channels 502. Optical channels 514 and 516 are placed on opposite sides of channels 502, where laser light from a laser source, not shown, may be directed from the wall of optical channel 514 through the channels 512 and reflected to a waste dump by optical channel 516. A fluorescent detector, not shown, detects fluorescence in the region of the light path between optical channels 514 and 516.

In the above process, the two etching steps to create the V grooves and the vertical sidewall channels can be swapped without changing any other processing steps. Also, other techniques such as aligning wafers to different orientations can be used to create the 45° angle channels. The chip design is as depicted in Fig. 5, where the chip dimensions are 4 in square; the cast chips were enclosed with Mitsubishi films UN6.53.91 and UN6.53.92.

To test the effectiveness of the devices for irradiation of fluorophores and reduction of autofluorescence of the chips, a video was taken of the events using a camera attached to an inverted epifluorescence microscope with 488nm laser at 6mW and viewing through an OG bandpass filter. A 100μM TAMRA solution in TBE was employed and diluted in TBE to the final concentration. All images were viewed through a 10 x 10.45 objective, except the phase contrast image, which used a 20 x 10.45/Ph 1 objective.

Clear bands were observed with both 1μM and 10nM TAMRA. Little, if any, autofluorescence was observed. With dye in the channels, a sharp band of fluorescence is observed at the sight where the light beam passes through the TAMRA containing solution, with the fluorescence intensity related to the concentration of the fluorophore. Above the chip, a scatter pattern is observed, probably resulting from multiple reflections in the chip.

It is evident from the above results that the subject invention provides for an efficient method for irradiating fluorophores in microchannels or capillary channels, or it parallel streams lacking physical dividers. The irradiation light can be efficiently  
5 directed across a number of streams without causing significant autofluorescence and provides for sharp bands in the detection region. The devices can be readily manufactured to provide the desired channels and then be used for performing a variety of operations in a channel where the outcome is a fluorescent signal.

10 Although the foregoing invention has been described in some detail by way of illustration and example for purposes of clarity of understanding, it will be readily apparent to those of ordinary skill in the art in light of the teachings of this invention that certain changes and modifications may be made thereto without departing from the spirit or scope of the appended claims.

15

## WHAT IS CLAIMED IS:

1. An irradiation device for irradiating a plurality of streams of capillary dimensions,

5 said device comprising:

a substrate comprising a streambed for receiving a plurality of liquid streams of capillary dimensions;

10 capillaries in liquid transferring relationship with said streambed for forming said streams;

optical channels on opposite sides of said stream bed having reflecting walls confronting said streambed and parallel to the direction of said streams, one of said reflecting walls positioned for receiving light from light source and directing said light 15 orthogonally through said streambed, and the other of said optical channels for reflecting said light to a light dump.

2. An irradiation device according to Claim 1, further comprising a laser light 20 source positioned at an angle to said reflecting wall to direct light orthogonally from said reflecting wall.

3. An irradiation device according to Claim 2, wherein said reflecting walls are at 25 45° to the base of said device.

4. An irradiation device according to Claim 1, wherein said reflecting walls are at 45° to the base of said device.

30 5. An irradiation device according to Claim 1, wherein said streambed further comprises separating walls for physically separating said streams in said streambed, said separating walls at a 90° to said streambed.

6. An irradiation device according to Claim 1, further comprising sheath flow capillaries interspersed between said stream capillaries to provide liquid separation between said streams.

5 7. An irradiation device for irradiating a plurality of streams of capillary dimensions,  
said device comprising:

10 a substrate comprising a streambed for receiving a plurality of liquid streams of capillary dimensions, said streambed divided into a plurality of capillary-sized zones by perpendicular walls;

capillaries in liquid transferring relationship with said streambed for forming and directing individual streams into said zones;

15 electrokinetic means for moving said streams from said capillaries into said streambed; and

20 optical channels on opposite sides of said stream bed having reflecting walls confronting said streambed and parallel to the direction of said streams, one of said reflecting walls positioned for receiving light from light source and directing said light orthogonally through said streambed, and the other of said optical channels for reflecting said light to a light dump.

25 8. An irradiation device according to Claim 7, wherein said reflecting walls are coated with a reflecting coating.

9. An irradiation device according to Claim 7, wherein said optical channels are filled with air.

30 10. An irradiation device according to Claim 7, wherein said substrate is plastic and said stream bed is enclosed.

11. An irradiation device for irradiating a plurality of streams of capillary dimensions,  
said device comprising:

5 a substrate comprising a streambed for receiving a plurality of liquid streams of capillary dimensions;

a cover enclosing said streambed;

10 capillaries in liquid transferring relationship with said streambed for forming said streams;

electrokinetic means for moving said streams from said capillaries into said streambed;

15 sheath flow capillaries interspersed between said capillaries;

a reservoir in fluid transfer relationship with said sheath flow capillaries; and

20 optical channels on opposite sides of said stream bed having reflecting walls confronting said streambed and parallel to the direction of said streams, one of said reflecting walls positioned for receiving light from light source and directing said light orthogonally through said streambed, and the other of said optical channels for reflecting said light to a light dump.

25 12. An irradiation device according to Claim 11, wherein said reflecting walls are have a reflecting coating.

30 13. An irradiation device according to Claim 11, wherein said optical channels are filled with air.

14. In a method for detecting the result of an operation resulting in a fluorescent signal in a capillary, the improvement which comprises:

detecting said fluorescent signal by irradiating said capillary in an irradiation device according to Claim 1.

5 15. A method according to Claim 14, wherein said fluorescent signal is a result of the binding of a labeled ligand to a receptor.

10 16. A method according to Claim 14, wherein said fluorescent signal is a result of an enzyme reaction on a non-fluorescent substrate to produce a fluorescent product.

15 17. An irradiation device according to Claim 7, wherein said reflecting walls have a reflecting coating.

18. An irradiation device according to Claim 7, wherein said optical channels are filled with air.

20 19. A method for producing an irradiation device according to Claim 1, employing a silicon wafer cut about  $9.74^\circ$  off the  $\langle 100 \rangle$  orientation, said method comprising:

25 growing a silicon dioxide layer on at least one surface of said silicon wafer;

photolithographically etching one side of said wafer to remove said silicon

30 dioxide at predetermined sites for channel formation;

coating said silicon wafer on at least said side with silicon nitride;

photolithographically etching to expose said silicon at an optical channel

site;

etching said optical channel site with hydroxide at an elevated  
temperature

5 to produce a V-shaped groove at a 45° angle;

oxidizing the silicon in the optical channel to silicon dioxide;

removing remaining silicon nitride; and

10

etching said silicon wafer through said silicon dioxide layer to form  
channels with 90° separating walls.

20. A method of producing a silicon chip having microstructures in said silicon chip  
15 of different cross-sections and depths of greater than about 5μm, said method  
comprising:

coating both sides of a silicon wafer with silicon dioxide;

20

spin-coating both sides of said wafer with photoresist;

photolithographically patterning a first side of said wafer to define sites for at  
least one of a first set of microstructures;

25

etching said silicon dioxide layer at sites above areas to be etched to form at  
least one of a first set of microstructures;

stripping said photoresist from said wafer;

30

depositing a silicon nitride layer on both sides of said silicon wafer;

spin-coating on both sides of said wafer with photoresist;

photolithographically patterning said first side of said wafer to define sites for at

35

least one of a second set of microstructures having a different cross-section from said  
first set of microstructures;



etching said silicon nitride layer at sites above areas to be etched to form said second set of microstructures and silicon dioxide underneath said silicon nitride layer to expose said silicon wafer;

5

etching said silicon wafer to form at least one of said second microstructures;

coating exposed portions of said wafer with silicon dioxide;

10

removing remaining portions of said silicon nitride layer and exposing said wafer at said sites of said at least one of a first set of microstructures; and

etching said silicon wafer to form said first microstructures.

15

21. An irradiation device for irradiating a stream of capillary dimensions, said device comprising:

a substrate comprising a streambed for receiving a liquid stream of capillary dimensions;

20

one or more capillaries in liquid transferring relationship with said streambed for forming said stream;

25

an optical channel on one side of said stream bed having reflecting walls confronting said streambed and parallel to the direction of said stream, one of said reflecting walls positioned for receiving light from light source and directing said light orthogonally through said streambed.

30

22. An irradiation device according to Claim 21 further comprising an optical channel on the other side of said stream bed having reflecting walls confronting said streambed and parallel to the direction of said stream, one of said reflecting walls positioned for reflecting light from said streambed to a light dump.

23. An irradiation device according to Claim 21 wherein said substrate comprises a streambed for receiving a plurality of liquid streams of capillary dimensions for irradiation.

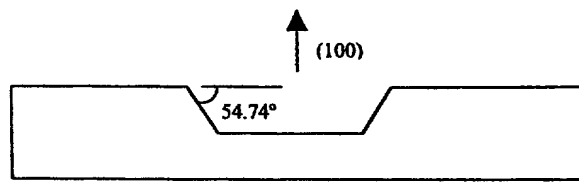
5 24. An irradiation device according to Claim 22 wherein said substrate comprises a streambed for receiving a plurality of liquid streams of capillary dimensions for irradiation.

**SIDE LIGHT ACTIVATED MICROFLUID CHANNELS**

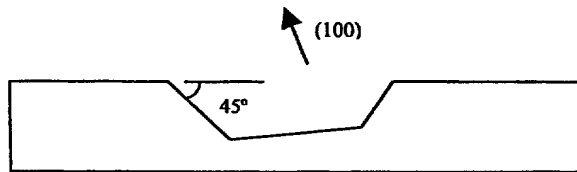
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**ABSTRACT OF THE DISCLOSURE**

Devices are provided comprising reflecting channels for directing horizontally a vertical light beam through a multiplicity of streams. The devices provide particular application in microfluidics, where streams of narrow cross-sectional dimensions are involved. Desirably, channels having 45° walls are provided for directing the beams and where the streams are divided by walls, the walls are at substantially 90°. Methods of fabrication of the devices are provided.



(a)



(b)

Figure 1

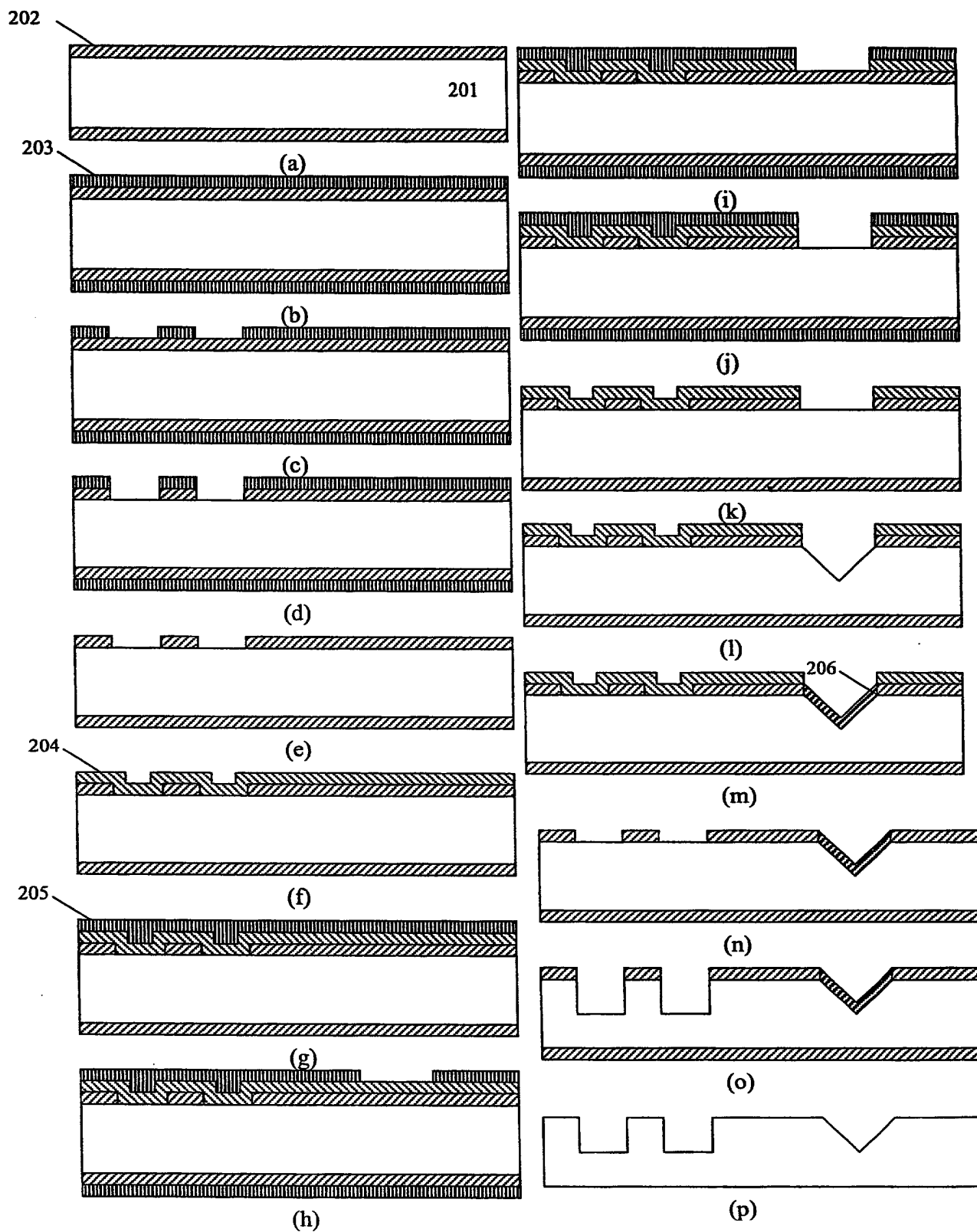
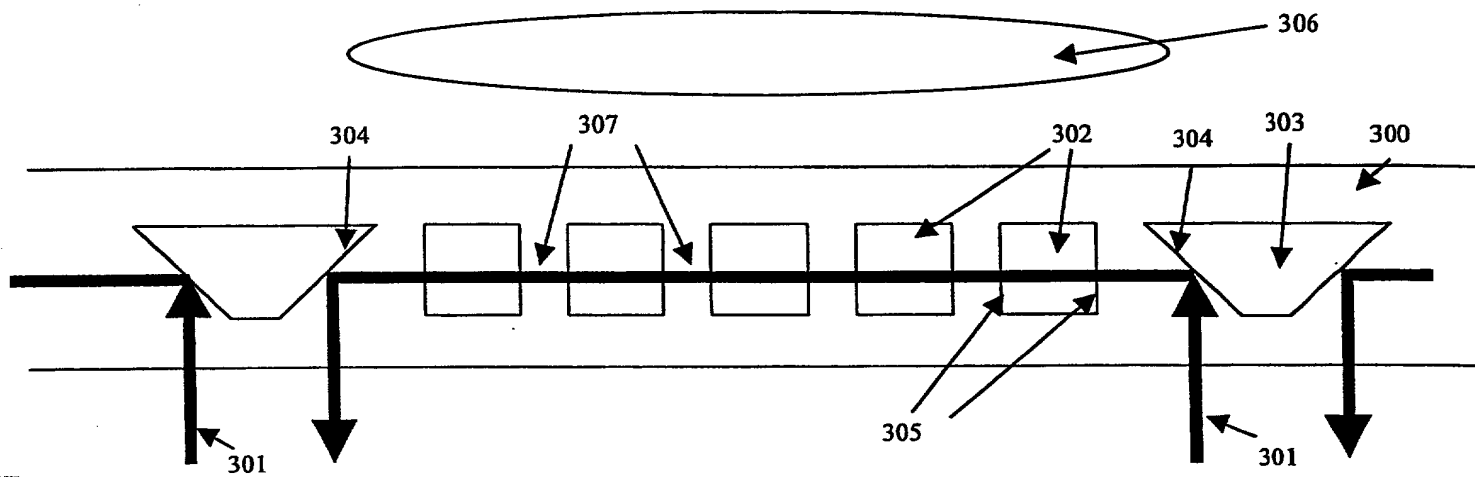
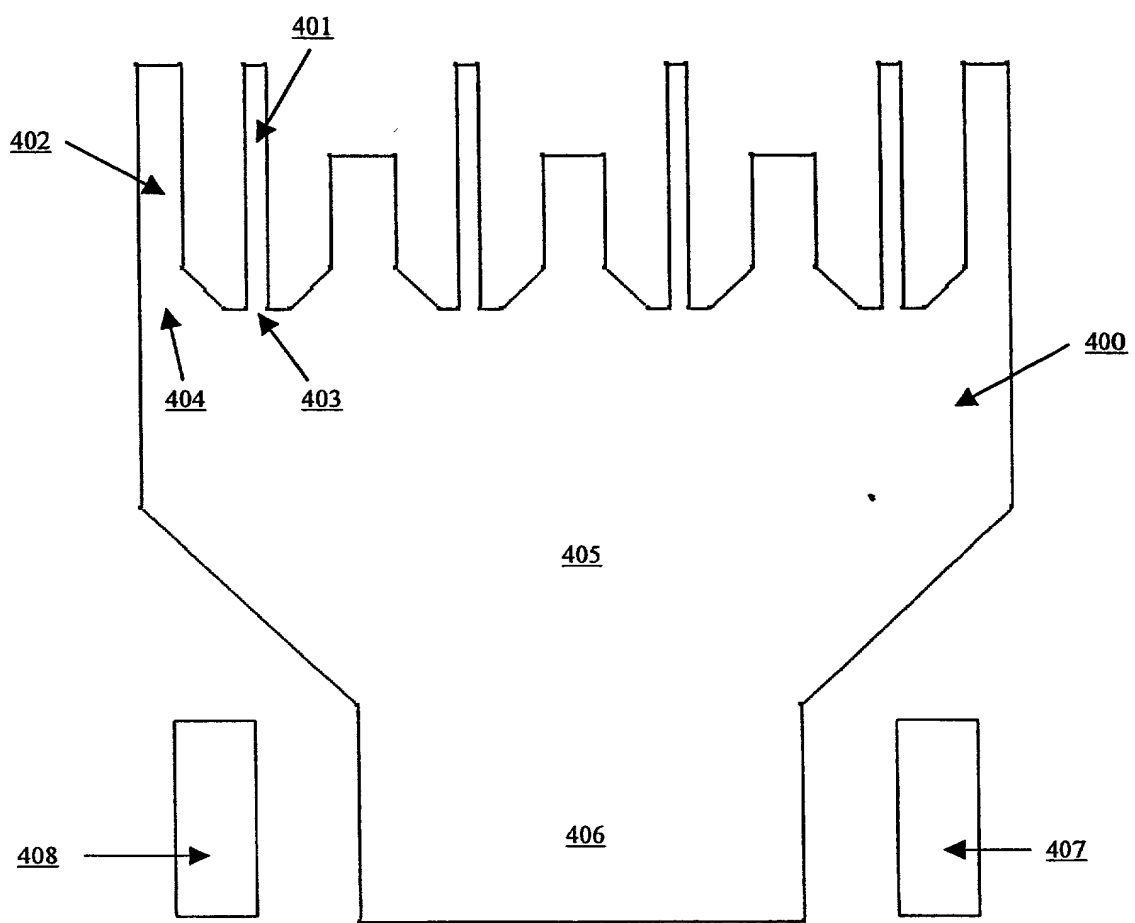


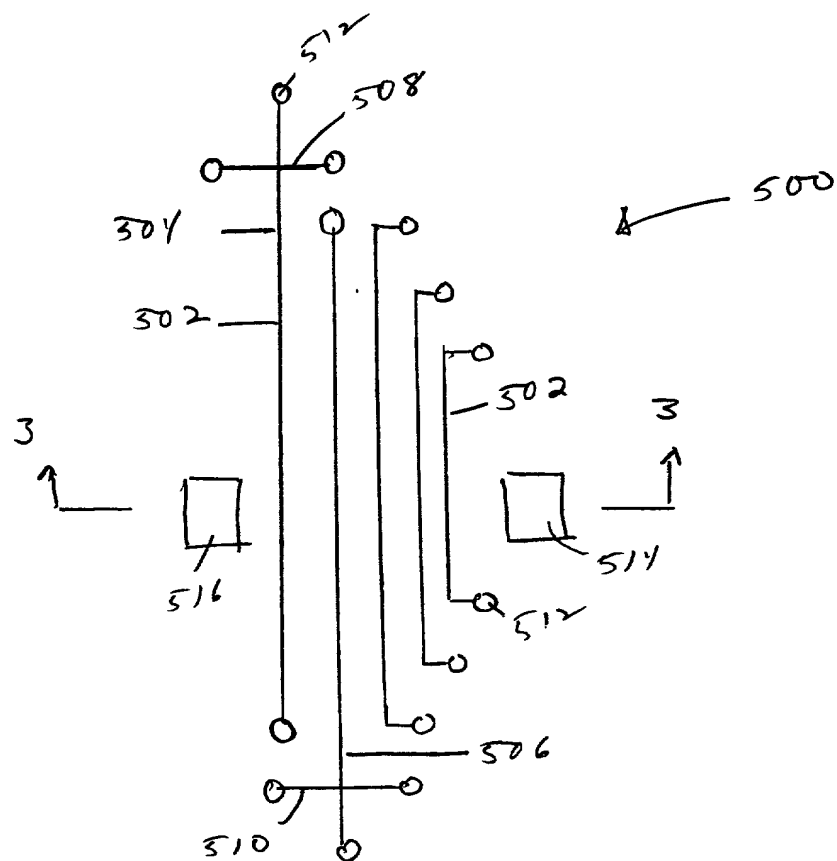
Figure 2



**Figure 3**  
**3 of 6**

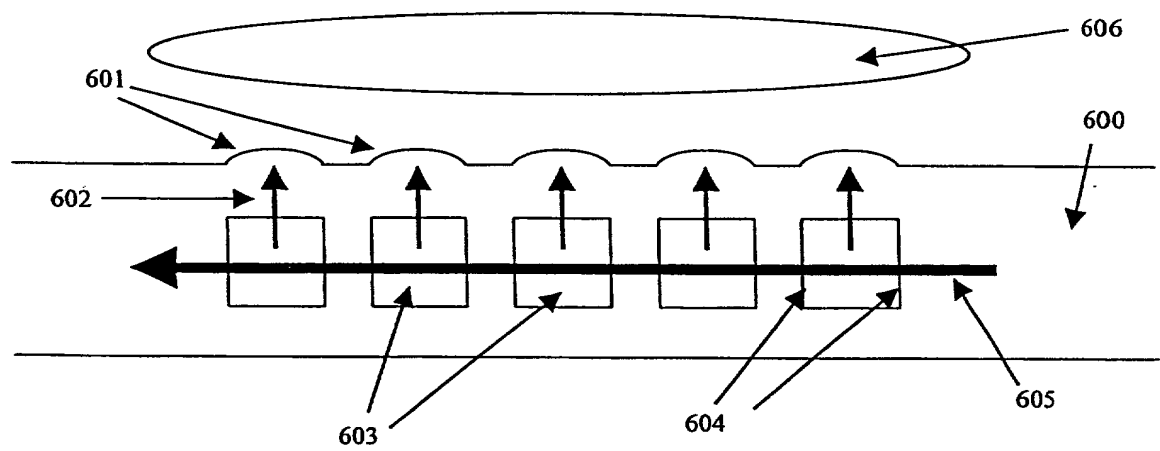


**Figure 4**  
**4 of 6**



### Figure 5





**Figure 6**

As a below-named inventor, I hereby declare that:

My residence, post office address and citizenship are as stated below next to my name, I believe I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled:

the specification of which

(check one) ☐ Is attached hereto.  
☐ Was filed on \_\_\_\_\_ and has been assigned Serial Number \_\_\_\_\_.  
☒ Was filed on September 13, 2000 as Attorney Docket No. ACBI.048.01US.

I hereby state that I have reviewed and understand the contents of the above-identified specification, including the claims, as amended by any amendment referred to above.

I acknowledge the duty to disclose all information known to me to be material to patentability of this application as defined in Title 37, Code of Federal Regulations, §1.56 and, if applicable, all such information under 37 CFR § 1.56 which became available between the national or PCT International filing date of the prior application and the filing date of this application.

I hereby claim foreign priority benefits under Title 35, United States Code, §119 of any foreign application(s) for patent or inventor's certificate listed below and have also identified below any foreign application for patent or inventor's certificate having a filing date before that of the application on which priority is claimed:

Prior Foreign Application(s)

Priority Claimed

| (number) | (Country) | (Day/Month/Year Filed) | <input type="checkbox"/> Yes | <input type="checkbox"/> No |
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Prior Foreign Application(s)

Priority Claimed

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| 70       | USA       | 10/10/2018             |                              |                             |

Prior Foreign Application(s)

Priority Claimed

(number)

(Country)

(Day/Month/Year Filed)

☐ Yes

☐ No

I hereby claim the benefit under Title 35, United States Code, §119(e) of any provisional application and §120 of any non-provisional United States application(s) listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States application in the manner provided by the first paragraph of 35 USC §112 I acknowledge the duty to disclose all information known to me to be material to patentability as defined in Title 37, Code of Federal Regulations, §1.56, which became available between the filing date of the prior application and the national or PCT international filing date of this application:

60/153,644

September 13, 1999

Pending

(Application Serial No.)

(Filing Date)

(Status)

(Application Serial No.)

(Filing Date)

(Status)

(Application Serial No.)

(Filing Date)

(Status)

\* designating the U.S.

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JENNIFER WAHLSTEN, Ph.D., Reg. No. 46,226

as my attorneys or agents with full power of substitution and revocation to prosecute my above-identified application for Letters Patent and to transact all business in the Patent Office connected therewith.

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I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Title 18, United States Code, §1001 and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

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